

DIGITAL IMAGE PROCESSING
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WIRELESS CONTROLLER FOR MULTIPLE ROBOTS
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DESIGN &IMPLIMENTATION OF ENCODER FOR BINARY BCH CODE
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SMART ROBOT
DESIGN OF 9-BIT UART USING VERILOG HDL
ANALYSING THE IONOSPHERIC SCINTILLATIONS GPS SIGNALS
PC BASED SIMULATOR FOR TESTING A SUB SYSTEM CALLED SYNTHETIC MODULE EW SYSTEM
IMPLEMENTATION OF SHA-1 ALGORITHM OF FPGA
EMBEDDING DATA IN IMAGE USING SPREAD SPECTRUM
IMPLEMENTATION OF BILATERAL & FILTER PDR
GSM BASED DIGITAL NOTICE BOARD

CELL PHONE OPERATED ROBOTIC VEHICLE
DESIGN OF GSM FOR VENDING MACHINE
INTERNET OF THINGS BASED INDUSTRIAL AUTOMATION
DESIGN & IMPLIMENTATION OF TRAFFIC LIGHT CONTROLLER USING VERILOG
TIMER BASED AUTOMATIC POWER CUT OFF FOR INDUSTRIAL SEALING PACKING MACHINESS
SPEAKER RECOGNISATION USING EFFIECION CORRECTION APPROACHES MATLAB
DEVELOPMENT OF REAL TIME DATA LOGGING FROM TWO SYSTEMS USING RS 232
IMPLIMENAONOF INDIAN CURRENCY DENOMINATION RECOGNISATION SYSTEM
DESIGN IMPLIMENTATION OF A LAW USING VLAN VTP HTTP AND ALONG WITH CONFIGURATION
CONTROLLING OF ROBOT & VIDEO RECEPTION APC
DIGITAL IMAGE CONFIDENCIALITY USING ARNOLD TRANSFPRMATION
VHDL IMPLIMENTQTIONOF OFDM DATA TRANSFER CONTROLLER
PHASE NOISE ESTIMATION IN MIMO SYSTEM
MISSILE TRACKING AUTO COLLISINE SYSTEM
DESING OF JPEG COMPRESSION STANDARD
OPTIMIZED MULTIPLIER USING REVERSIBLE LOGIC GATES A VEDIC MATHEMETICS APPROACH
MITIGATION OF CORPORATE NETWORKS FROM IPV4 IP V6 USING TUNNEL
SIMULATION OF DEVELOPMENT OF BPSK TRANSMITTER USING AWR
ARM BASED AES ALGORITHM FOR WIRELESS SENSOR NETWORKS
IMPLIMENTATION OF EIGRP ON IPV6
HDLC PROTOCOL CONTROLLER FOR EMBEDDED SOC
DESIGN OF MULTI OF PCI-XP PHYSICAL LAYER TRANSMIT PROTOCOL
DTMF AND GROUP 3 FAX DETECTION USING MATHS
QUIRY BY HUMING FOR MUSIC RETRIVAL SYSTEM
GENERATION OF ARBITRARY WAVEFORM FOR RADAR APPLICATION
PROGNOSTIC MODEL FOR GENOMIC DISCOVERY TO BIPOLAR DISCOVERY
PERFORMANCE OF DATA COMPRESSION TECHNIQUES ON LOW POWER EMBEDDED PROCESSOR
HOME CONTROLLING SYSTEM USIGN ZIGBEE MODULE
AUTOMATIC ENERGY METER READING AND CONTROL SYSTEM USING RF AND GSM
TEST SIM ULATOR FOR SYNTHETIC CHANNEL
KEYLESS ENTRY CRUISE WITH ALERT SYSTEM FOR AUTOMOBILES
FAULT SECURE ENCODER AND DESI-ORDER FOR MEMORY APPLICATIONS
EVENT SHEDULAR BASED ON 12 C ENABLE RTC AND EMBEDDED MICROCONTROLLER
FIRMWARE AND HARD WARE DESIGN OF A NOVEL DOOR LOCKING SYSTEM
ZIGBEE WIRELESS SENSOR NETWORKS IN INDUSTRIAL OPTICAL
DESIGN SIMULATION FABRICATION AND TESTING OF MICROSTRIP ANTENNA
PERFORMANCE ANALYSIS OF ROUTING RPOTOCOL IN ANTENNA
INTERPROCESS COMMUNICATION METHODS FOR MULTI CPU EMBEDDD SYSTEM
DESIGN AND SIMULATION OF WI-FI MAX DATA LINK LARGER
IMPLIMENTATION OF SECURITY IN WAN
DESIGN AND IMPLIMENTATION OF 64 BIT RISC PROCESSOR
AUTOMATION OF CHEMICAL CLEARING OF OPTICAL COMPONENS IN A CLEAR ROOM
FAST INTRA REDIATION ALGORITHM USING H-264/ARC VIDEO COMPRESSOR
BUILDING ON AT 89552 VOICE ACTIVATED ROBO BASED ON DTMF TECCHNIQUE
X-RAY SENSOR AMPLIFIER PRINTED CIRCUTED BOARD
CODE PLANNING OF 3G UMTS NETWORKS USING ATOLL
PERFORMANCE OF DESIGN AND DEVELOPMENT OF BOW -TIE ARRAY ANTENNA
SYNTHESISZABLE IP-CARE SECURE ALGORITHM OF WI-FI
MULTI SENSOR STRATEGY FOR THE BLIND PERSONS WITH GPS TRACKING
IMPLIMENTATION OF FAST MOTION ESTIMATION ALGORITHM FOR COMPRESSION USING H.264
INTERFACING OF CHILLER CONTROL UNIT TO UTM PC USING MICROCONTROLLER
PC-BASED INDUSTRIAL DEVICE CONTROLLING BASED ON ARM MICROCONTROLLER
MULTI SENSOR EMBEDDED SYSTEM FOR AGRO INDUSTRIAL APPLICATIONS
AERONATICAL CRAGE PREVENTION SYSTEM THROUGH ZIGBEE TECHNOLOGY
E-PASSPORT FUTURE OF GOVERNMENT ISSUED RFID BASED IDENTIFICATION
DESIGN AND IMPLIMENTATION OF GIGABIT EICHER MAC FRAME USING VERLOG
IMPLIMENTATION OF ADVANCED ENCRYPTION STANDARD CAES-(128)USING VHDL
IMPLIMENTATION OF CMOS CELL STRUCTURE USING LOW POWER & HIGH PERFORMANCE GDI TECHNIQUES
OF LINK ENGINEERING AND PRACTICAL INPLIMENTATION OF SDH SYSTEM OF STM-16
BENCH MAKING OF NETWORK KEY PERFORMANCE INDICATORS IN MOBILE SERVICE URBAN AREA
DESIGN AND TESTING OF DORECTIONAL COMPLER
OPTIMIZATIN OF ENERGY CONSUPTION BY ADDING RELAY NODES IN WIRELESS SENSOR NETWORK
BIT ERROR RATE PERFORMANCE OF CDMA USING MATLAB AND SIMULATION

DESIGN KEY EXPANDER FOR 128 BIT AES ALGORITHM
TCP/IP ON INDUSTRIAL AUTOMATION
DEVELOPMENT OF HUFFMAX ENCODER AND DECODER IN FPGA
K-RLE DATA COMPRESSION ALGORITHM FOR SENSOR DATA ON FPGA
DYNAMIC RANGE DETRECTION UNIT FOR HIGH SPEED MULTIPLIER USING FRGA
DESIGN CONFIGURABLE OFDM DATA PROCESSOR USING WIMAX
SYNTHESIS AND DESIGN OF PARAMETER EXTRACTOR FOR LAW POWER PBCAM USING GATE BLOCK
MULTISENSOR STRATEGY FOR THE BLING PERFORM WITH GAS
AN ADAPTIVE HANDS-FACE TECHNIQUE FOR MOBILE COMMUNICATION SYSTEM
COOPERATIVE SPECTRUM SENSING IN COGNITIVE PADIO NETWORKS
DESIGN AND DEVELOPMENT OF WIDE - MULTIBAND MICROTRIP SINGLE PATCH ANTENNA FOR BAND GPS AND IRNSS
ULRASONIC MAP MAKER
WIRELESS SENSOR NETWORKS MODE IMPLIMENTATION USING XBEE ON ARDUINO PLATFORM
DUAL POWER GENERATION WITH SOLAR TRACKING SYS & WIND ENERGY
PERFORMANCE EVALUATION LEACH & SEP PROTOCOLS FOR WSN
ACCIDENT DETECTION & PERFORMING SYS
AQUABOT
DESIGN OF EQUIRIPPLE LINEAR PHASE FIR FILTER USING REMEZ ALGORITHM
PORTABLE PEDESTAL TRACKING FOR TELEMETRY USING MICROCONTROLLER
CONTROLLER DESING FOR REMODULE
AUTOMATIC BELL SYS SCHOOLS/ INSTITUTIONS
UAV FOR WIFI RECEIVER MAPPING & PACKET SNIFFING
RF FREQUENCY SYNTHESIZER PROGRAMMIN USING MICROCONTROLLER
FINGER PRINT & AADHAAR NUMBER BASED BIOMETRIC VOTING MACHINE USING ARDUINO
QUALITY OF EXPERIENCE ASSESMENT & ITS APPLICATION TO VIDEO SERVICES ON LTE NETWORKS
IMPROVED HANDOVER THROUGH DUAL CONNECTIVITY IN 5G MM MOBILE NETWORKS USING ANALYTICAL MODELS
QUEUE STABILITY IN ENERGY HARVESTING COGNITIVE RADIO NETWORKS A STOCHASTIC STACKELEBERG GAME APPROACH
RASH DRIVING DETECTING SYS
LOW COMEXLITY
KEY FRAME EXTRACTION OF VIDEO SEQUANCE DATA
MAINTANANCE SCHEDULING SYS USING TWITTER FEED
ZIGBEE &GSM BASED VARIABLE PHYSIOLOGICAL PARAMETERS MONITERING SYS
E/J BAND FIELD SIGNAL GENERATER
WIRELESS VIDEO CAM SURVEILLANCE &ROVER CONTROL
SPEECH ENHANCEMENR USING MAGNITUDE & PHASE SPECTRUM
BRAIN TUMOR DETECTION USING HYBRID ALGORITHM
FACE RECOGNISATION BASED ATTENDANCE SYS
IMPLIMENTATION OF SAFETY SYS FOR COAL MINES USING ARDUINO
DESIGN AND DEVELMENT OF AOA COMPUTATION PCB FOR INTEGRATED COMMUNICATION SYS
DESIGN & IMPLIMENTATION OF SIGNAL WAVE GENERATION FOR SONAR APPLICATION
DESIGN & IMPLIMENTATION OF AZIMUTH & ELEVATION POSITION CONTROLLER
WOMENS SAFETY DEVICE
INTERACTIVE VOICE RESPONSE SYS FOR COLLEGE AUTOMATION
BONE FRACTION DETECTION USING IMAGE SEGMENTATION
IMAGE QUALITY ASSESMENT FOR FAKE BIOMETRIC DETECTION
MOBILE BASED HEALTH MONITERING SYS USING GSM& MODULES
MONITERING SYS OF MISSILE
A CAN PROTOCOL BASED EMBEDDED SYS TO A REAR- END COLISSION OF VEHICLE
DESIGN & IMLIMENTATION OF AREA EFFICIENT DISTRIBUTED ARITHMETIC USING DIVIDED LUT ARCHITECTURE
FREQUENCY & POLARIZATION RECONFIGURALE ANTENNA
BRAIN TUMOR DETECTION USING K MEANS CLUSTERING
CLIMATE MONITERING SYS & TRANSMISSION OF DATA WITHOUT INTERNET
VRICATION OF AHB PROTOCOL USING UVM
VISIBILITY METRIC ENHANCEMENT FOR HAZE IMAGES
ADVANCED TRANSMISSION LINE SAG DETECTION SYS
HOME AUTOMATION USING RASPBERRI PI
EXPLORING ADDING SCHEMES FOR THE IMPROVEMENT OF OPTIMIZED RADIX -2 & RADIX-4 FFT BUTTERFIES
CONTROLLER FOR BPI FLASH USING VERILOG
ANALYSIS OF VEDIC MULTIPLIER USING VARIUS ADDER TOPOLOGIES
POLARIZATION RECONFIGURABLE ANTENNA
DESIGN OF 64 BIT BARREL SHIFTER USING VERILOG
DESIGN& ANALYSIS OF DUAL BAND MICROTRIP PARCH ANENNA

RFID & BARCODE BASED LIBRARY MGT SYS
ONE SHOT BLIND CHANNEL ESTIMATION FOR OFDM SYS
AUTOMATIC IRRIGATION SYS
IMPLIMENTATION OF STEGANDGRAPHY USING ARNOLD TRANSFORM
DIPOLE ANENNA FOR TELEMETRY APPLICATION
DESIGN & DEVELOPMENT OF GSM 7 GPS BASED VEHICLE THEFT CONTROL SYS
IMPLIMENTATION OF OFDM SYS USING MATLAB
SIMULATION FABRICATION & TESTING OF DUAL FREQUENCY RECONFIGURABLEEE ANTENNA
RID BASED SMART TROLLEY
COMPUTER VISION BASED OBJECT TRACKING
HAND GESTURE CONTROLLED ROBOTIC ARM
AN ADVANCED SECURITY SYS INTEGRATED WITH RFID BASED AUTOMATED TOLL COLLECTION SYS
ARDUINO BAED FIRE FIGHTING ROBOT
FAST IMAGE ENCRYPTION BASED ON RANDOM IMAGE KEY
ATM SECURITY SYS USING METAL DETECTOR & VIBRATION SYS
A STUDY OF RECURSIVE LEAST SQUARES ADAPTIVE FILTER ALGORITHM IN NOISE REMOVAL FROM ECG SIGNALS
SMARTCARD & GPS BASED AUTOMATIC BUS TICKETING SYS
EXAMINATION ROOM GUIDE USING RFID AND FINGERPRINT MODULE
WAVELESS TRANSFORM BASED BRAIN TUMOR DETECTION USING HAAR ALGORITHM
DESIGN AND IMPLIMENTATION OF BLACK BOX IN AUTOMOBILES
FOUR CHANNEL WIRELESS AUDIO AMPLIFIER USING BLUITOOTH AND TE 8210 AH
ADAPTIVE TRAFFIC
DESIGN OF ENERGY EFFICIENT MAC PROTOCOL
BLUETOOTH ROBOTIC LAWN MOWER POWERED BY SOLAR ENERGY
ADVANCED AUTOMOBILE SECURITY SYS USING BIOMETRIC AUTHENTICATION
HIGH SPEED EFFICIENT 16-BITCARRY SKIP ADDER
DESIGN A CARRY SELECT ADDER FOR LOW POWER CONSUMPTION
DESIGN OF ADVANCED ENCRYPTION STANDARD FOR 128 BIT KEY LENGTH
HIGH SPEED EFFICIENT 16-BIT CARRY SKIP ADDER
LUNG CANCER DETECTION ON CT IMAGES
X-BAND SUBDIVIDED BANDPASS FILTER FOR SATTELITE TRANSPONDER
IMAGE COMPRESSION USING RUN-LENGTH ENCODING
DESIGN DEVELOPMENT AND VERIFICATION OF SPI PROTOCOL
IMPLIMENTATION OF GPS STANDARD POSITIONING SERVICE AND PRECISE POSITIONING SERVICE
SPEED AND NUMBER PLATE DETECTION OF A VEHICLE
DESIGN SIMULATION FABRICATION AND TESTING OF FRACTAL ANTENNA
GRADING AND CLASSIFICATION OF RICE GRAINS USING NUERAL NETWORKS
LOW DELAY CROSS LAYER MAC PROTOCOL FOR WIRELESS SENSOR NETWORKS USING NS-3
RF ENERGY HARVESTING FOR SPECTURUM MGT IN COGNITIVE RADIO NETWORK
ENERGY ANALYSIS OF MULTIHUP PROPAGATION USING RASPBERRY PI-3
DUAL STEGANOGRAPHY A NEW HIDING TECHNIQUE FOR DIGITAL COMMUNICATION
ACCIDENT IDENTIFICATION AND ALERTNG SYSTEM
PLANT DISEASE DETECTION USING TENSORFLOW
AUTOMATIC PANTRY ORDER SYS USING ZIGBEE MODULE
TOUGH SCREEN.BASED DIGITAL SLATE FOR NEXT GENERATION
FACIAL RECOGNITION USING IOT
PROGRAMMABLE AUTOMATIC COLLEGE BELL
WSN BASED SECURITY SURVELLANCE SYS
IMPLEMENTATION OF DRIVER DROWSINESS DETECTION SYS
SPATIAL MODULATION IN MASSIVE MIMO WITH
SOLAR GRASS CUTTER WITH OBSTACLE AVOIDANCE
SPEAKING AID FOR DEAF AND DUMB
MOVING OBJECT DETECTION METHOD FOR VIDEO USING ARTIFICIAL INTELLIGENCE
RECOGNIZING CLOTHES PATTERNS AND COLOUR FOR BLIND PEOPLE USING MARLAB
PARALLEL PREFIX ADDER A COMPARATIVE STUDY OF FASTEST RESPONSE
IOT BASED AUTONOMOUS VEHICLE WITH THE AID OF COMPUTER VISION
BUILDING MONITERING SYS USING WIRELESS SENSOR NETWORKS
LOW POWER SAD COMPUTATION FOR A 4*4 IMAGE USING VERILOG HDL
COLOUR SORTER USING ARDUINO
DESIGN OF DAYADI 1-BIT CMOS FULL ADDER BASED ON POWER REDUCTION TECHNIQUES
DESIGN OF LOW POWER CMOS 3 INPUT XOR AND XNOR BASED ON VOLTAGE SCALING
DESIGNOF LOW POWER 1 BIT CMOS ALU
DATA TRANSMISSION THROUGH LI-FI TECHNOLOGY

DESIGN OF LOW POWER CMOS COMPRESSOR
DATA ENCRYPTION AND DECRYPTION FOR BLUETOOTH SECURITY
DUAL STEGANOGRAPHY A NEW HIDING TECHNIQUE FOR DIGITAL COMMUNICATION
A STUDY OF SIMULATION FRAMEWORK FOR COGNITIVE RADIO NETWORKS
DESIGN DEVELOPMENT OF MONOPLUS TRACKING MODULE FOR ESM RECEIVER
VEHICLE TRACKING USING GPS AND GSM
GESTURE CONTROLLED ROBOT USING GPS AND GSM
DESIGN OF 32 BIT RISC PROCESSOR IN VERILOG HDL
APB PROTOCOL DESIGN
SPATIAL MODULATION IN MASSIVE MIMO WITH OPSA
XOR FREE IMPLEMENTATION OF CONVENTIONAL ENCODER
IMAGE CRYPTOGRAPHY USING HAAR WAVELET TRANSFORM
ADVANCED BLANKING NON-LINEARITY FOR MITIGATING INTERFERENCE IN OFDM SYS
SMART SHOPPING CART WITH AUTOMATIC BILLING USING RFID AND ZIGBEE
LOW-POWER ADDITION WITH BORROW SAVE ADDER AND 4-BIT ARRAY MULTIPLIER
CONTENT BASED IMAGE RETRIEVAL USING CONVOLUTIONAL NEURAL NETWORKS
DESIGN SIMULATION & FABRICATION ARRAY OF TRIANGULAR MICROSTRIP PATCH ANTENNA
CROP PROTECTION USING IOT BASED WIRELESS SENSOR NETWORKS
CAMOUFLAGED SURVEILLANCE AND IMAGE DETECTION
HUMAN SAFETY WATCH
RAIL TRACK DETECTION & FAULT DIAGNOSIS USING IMAGE
DESIGN OF CONTROLLER FOR IN EMBEDDED SYS
WATER QUALITY MONITORING SYS BASED ON IOT
A COMPARATIVE PERFORMANCE ANALYSIS OF ROUTING PROTOCOLS IN MANET USING NS 3 SIMULATORS
SATELLITE IMAGE RESOLUTION ENHANCEMENT USING DT-CWT & ADAPTIVE BILATERAL FILTER
DESIGN OF LOW POWER CMOS 4-BIT ARRAY & TREE MULTIPLIER USING DSM TECHNOLOGY
5 GHZ WIDE RING COUPLER FOR RADAR
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ELECTRONIC NOTICE BOARD USING RASPBERRY P 13
IMPLEMENTATION OF GRAPHICAL USER INTERFACE FOR DIGITAL SINGLE
EMERGENCY BREAKING WARNING APPLICATION SYS USING NS-3 SIMULATION
HEAD MOVEMENT BASED VOICE ENABLED WIRELESS DEVICES SWITCHING FOR PHYSICALLY CHALLENGED
DESIGN AND IMPLEMENTATION OF LOW POWER LFSR FOR FAST ATPG PROCESS